

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MASATO SUZUKI et al.

Serial No.: 08/587,338

Filed: January 16, 1996

For: VARIABLE ADDRESS-LENGTH
COMPILER AND PROCESSOR
IMPROVED IN ADDRESS
MANAGEMENT (AS AMENDED)

Prior Examiner: S. Kang

Prior Group Art Unit: 2315

January 19, 1996

Irvine, California 92714



RECEIVED

JUL 23 1996

GROUP 2300

**SUPPLEMENTAL
PRELIMINARY AMENDMENT**

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

In response to the Office Action of August 15, 1995, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel Claims 28-36 without prejudice.

Please add the following new claims:

RECEIVED

MAR 07 1996

GROUP 2300

58. A computer system comprising a processor and a program converting unit,
wherein
said processor is one out of a series of embedded-type processors, each processor in the
series having a different address bit width N, N being longer than a data width M, the address
bit width N of said processor being selected in accordance with a program size,

300 SF 03/01/96 08587338
1 102 775000 LN
1 103 88.00 CK